



Form no: B Tech/ECE/Odd/.....

REGISTRATION FORM (B.Tech) ELECTRONICS&COMMUNICATION ENGINEERING DEPARTMENT

Name of the student:		Roll No:	
Mobile no:	Category: (SC/ST/OBC/GEN):	Email Id:	
Semester:	Year:	Branch:	
Father's Name & Mobile no:			

COURSES REGISTERED						
1 st Semester						
Sl. No	Subject Code (pls. tick opted code)	Subjects Name	Credit	Offering Department	Core/Elective	Remarks of HOD
1	MA11101 <input type="checkbox"/>	Mathematics I	4	Math		
2	PH11101 <input type="checkbox"/>	Engineering Physics	3	Physics		
3	EE11101 <input type="checkbox"/>	Principles of Electrical Engineering	3	EE		
4	CS11101 <input type="checkbox"/>	Computer Programming and Problem Solving	2	CS		
5	CS11102 <input type="checkbox"/>	Introduction to Computer Systems	2	CS		
6	HS11101 <input type="checkbox"/>	English Language and Literature	3	HSS		
Practicals and Sessionals						
7	CS11201 <input type="checkbox"/>	Computer Programming Laboratory	2	ECE		
8	PH11201 <input type="checkbox"/>	Engineering Physics Laboratory	1	ECE		
9	EE11201 <input type="checkbox"/>	Electrical Workshop	1	ECE		
10	ME11201 <input type="checkbox"/>	Workshop Practice	2	ECE		
3 rd Semester						
1	MA13101 <input type="checkbox"/>	Computational Mathematics	4	Math		
2	EC13101 <input type="checkbox"/>	Network Analysis and Systems		ECE		
3	EC13102 <input type="checkbox"/>	Signal and System	3	ECE		
4	EC13103 <input type="checkbox"/>	Semiconductor Devices	3	ECE		
5	EC13104 <input type="checkbox"/>	Probability Theory and Stochastic Process	3	ECE		
6	CS13101 <input type="checkbox"/>	Data structure and Algorithm	3	ECE		
Practicals and Sessionals						
7	CS13201 <input type="checkbox"/>	Data Structure and Algorithm Laboratory	1	CS		
8	EC13201 <input type="checkbox"/>	Network Analysis and Synthesis Laboratory	1	ECE		
9	EC13202 <input type="checkbox"/>	Signal and System Laboratory	1	ECE		
10	EC13203 <input type="checkbox"/>	Semiconductor Devices Laboratory	1	ECE		
11	ZZ13201 <input type="checkbox"/>	Professional Practice II	Audit			
5 th Semester						
1	HS15101 <input type="checkbox"/>	Engineering Economics	2	HSS		
2	ECE15101 <input type="checkbox"/>	Digital Communication	3	ECE		
3	ECE15102 <input type="checkbox"/>	Linear Integrated Circuits	3	ECE		
4	ECE15103 <input type="checkbox"/>	Digital Signal Processing	3	ECE		
5	ECE15104 <input type="checkbox"/>	Microwave Engineering	3	ECE		
6	<input type="checkbox"/>		3		Elective	
Practicals and Sessionals						
7	EC15201 <input type="checkbox"/>	Digital Communication Laboratory	1	ECE		
8	EC15202 <input type="checkbox"/>	Digital Signal Processing Laboratory	1	ECE		



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9	EC15203	<input type="checkbox"/>	Integrated Circuit Laboratory	1	ECE		
10	EC15204	<input type="checkbox"/>	Electromagnetics and Antenna Laboratory	1	ECE		
11	ZZ15201	<input type="checkbox"/>	Professional Practice IV	Audit	ECE		
7th Semester							
1	MS4003	<input type="checkbox"/>	Economics	3	HSS		
2	EC4011	<input type="checkbox"/>	Fundamentals of Wireless Communication	4	ECE		
3	EC4091	<input type="checkbox"/>	Digital Signal Processing Lab	2	ECE		
4	EC4092	<input type="checkbox"/>	Digital Communication Lab	2	ECE		
5	EC4098	<input type="checkbox"/>	Major Project	3	ECE		
6						Elective	
7						Elective	

Note: The back log subjects, if any are to be tick mark for preceding ODD semester.

FOR OFFICE USE ONLY	No of subjects registered for current semester:	No of backlog subjects of previous semester: (Tick Above)
	_____	_____

Tuition Fees Payment mode: Online Transfer/DD (<i>PlsTick</i>)			
DD/Transaction no/Challan .No:			
DD/Online transfer/ Challan Date:			
Date:	Signature of the student:		
Date:	Seal & Signature of the HOD/FIC:		
Student Affairs Clearance	Comment:.....		
	Dean Student Affairs		
FOR OFFICE ONLY	Date of Form Receipt	Received after verification by:	Fee remittance confirmed at Bank
			Accounts Office